



QSFP28

#### EQ2xx10X-3LCD2

#### 100Gb/s QSFP28 CWDM4 2KM Optical Transceiver

- > Four-channel full-duplex transceiver modules
- > Transmission data rate up to 26Gbit/s per channel
- > Up to 2km transmission of single mode fiber
- ➢ Low power consumption <3.5W</p>
- > Operating case temperature  $0^{\circ}$ C to +70°C
- > 3.3V power supply voltage
- RoHS 6 compliant
- Hot Pluggable QSFP form factor
- LC connector receptacle
- Built-in digital diagnostic function



# Applications

- > 100G Ethernet
- Proprietary High Speed Interconnections
- > Datacenter
- > 100G CWDM4 application with FEC

# Description

This product is a Four-Channel, Pluggable, dual LC, Fiber-Optic QSFP28 Transceiver for 100G Ethernet applications. The QSFP28 full-duplex optical module offers 4 independent transmit and receive channels, each capable of 26Gbps operation for an aggregate data rate of 104Gbps 2km using single mode fiber. These modules are designed to operate over single mode fiber systems using 1271nm-1331nm DFB laser array. QSFP28 CWDM4 is one kind of transceiver which provides increased port density and total system cost savings. They are compliant with the QSFP28 MSA, CWDM4 MSA and portions of IEEE P802.3bm.

### **Regulatory Compliance**

Feature	Standard	Performance
Electromagnetic Interference (EMI)	FCC Part 15 Class B	Compatible with standards
	EN 55022:2010, Class B	
Electromagnetic susceptibility (EMS)	EN 55024:2010	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11	Compatible with Class I
	EN60950, EN (IEC) 60825-1,2	laser product

# Absolute Maximum Ratings

The operation in excess of any absolute maximum ratings might cause permanent damage to this module.

Parameter	Symbol	Min	Max	Unit	Notes
Storage Temperature	Ts	-40	85	°C	
Operating Case Temperature	T <sub>OP</sub>	0	70	°C	
Supply Voltage	Vcc	-0.5	3.6	V	
Input Voltage	Vin	-0.5	Vcc+0.3	V	
Relative Humidity (non-condensation)	RH	0	85	%	

#### **Recommended Operating Conditions and Power**

### **Supply Requirements**

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Operating Case Temperature	T <sub>OP</sub>	0		70	°C	
Power Supply Voltage	Vcc	3.135	3.3	3.465	V	
Data Rate, each Lane	DR		25.78125		Gb/s	
Power Consumption				3.5	W	
Data Speed Tolerance	ΔDR	-100		+100	ppm	
Link Distance with G.652	D	0		2	km	

# **Electrical Characteristics**

The following electrical characteristics are defined over the Recommended Operating Environment unless otherwise

#### specified.

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Differential input impedance	Zin	90	100	110	ohm	
Differential input impedance	Zout	90	100	110	ohm	
Differential input voltage amplitude	ΔVin	300		1100	mVp-p	
Differential output voltage amplitude	ΔVout	500		800	mVp-p	
Input Logic Level High	VIH	2.0		VCC	V	
Input Logic Level Low	VIL	0		0.7	V	
Output Logic Level High	VOH	VCC-0.5		VCC	V	
Output Logic Level Low	VOL	0		0.4	V	

# **Optical Characteristics**

Parameter	Symbol	Min	Typical	Max	Unit	Notes
	L0	1264.5	1271	1277.5	nm	
Wavelength Assignment	L1	1284.5	1291	1297.5	nm	
wavelength Assignment	L2	1304.5	1311	1317.5	nm	
	L3	1324.5	1331	1337.5	nm	
	_	Transm	itter			-
RMS Spectral Width	λrms			3.5	nm	1
Average Launch Power, each lane	PAVG			2.5	dBm	
Optical Modulation Amplitude (OMA)	РОМА	-4		2.5	dBm	1
Difference in Launch Power between any two lanes	Ptx,diff			4.0	dB	
Transmitter and Dispersion Penalty per Lane	TDP			3	dBm	
Rise/Fall Time	Tr/Tf			30	ps	
Extinction Ratio	ER	3.5			dB	
Transmitter Reflectance	RT			-12	dB	
Transmitter Eye Mask Margin	EMM	10			%	2
Average Launch Power OFF Transmitter, each Lane	Poff			-30	dBm	
Transmitter Eye Mask Definition {X1, X2, X3, Y1, Y2, Y3}		{0.31, 0.4, 0.45, 0.34, 0.38, 0.4}				

		Receiv	/er			
Damage Threshold	THd	3.5			dBm	
Overload, each lane	OVL	2.5			dBm	
Receiver Sensitivity in OMA, each Lane	SEN			-10	dBm	3
Signal Loss Assert Threshold	LOSA	-24		-13.6	dBm	
Signal Loss Deassert Threshold	LOSD			-11.6	dBm	
LOS Hysteresis LOSH			1.5		dB	
Optical Return Loss	ORL			-12	dBm	

#### Notes:

1. Transmitter wavelength, RMS spectral width and power need to meet the OMA minus TDP specs to guarantee link

performance.

- 2. The eye diagram is tested with 1000 waveform.
- 3. Sensitivity is specified at 5x10-5 BER.

# **Block Diagram of Transceiver**



## **Recommended Power Supply Filter**

The host board should use the power supply filtering shown as below.



#### **ESD**

This transceiver is specified as ESD threshold 1kV for SFI pins and 2kV for all other electrical input pins, tested per MIL-STD-883, Method 3015.4 /JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module. This transceiver is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

#### Laser Safety

This is a Class 1 Laser Product according to IEC 60825-1:2007. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (June 24, 2007).

#### **Digital Diagnostic Functions**

The following digital diagnostic characteristics are defined over the normal operating conditions unless otherwise specified.

Parameter	Symbol	Min	Max	Unit	Notes
					Over operating
Temperature monitor absolute error	DMI_Temp	-3	+3	°C	temperature
					range
Supply voltage monitor absolute error		0.1	+0.1	V	Over full
Supply voltage monitor absolute end		-0.1	+0.1	v	operating range
Channel RX power monitor absolute		3	3	dB	
error		-5	5	ЧD	
Channel Bias current monitor	DMI_lbias_Ch	-10%	+10%	mA	
Channel TX power monitor absolute		2	2	dP	
error		-3	3	UD	

# Pin Assignment and Description

38	GND		GND	1
37	TX1n		TX2n	2
36	ТХ1р		TX2n	3
35	GND		GND	4
34	TX3n		TX/n	5
33	ТХ3р		TX4n	6
32	GND		GND	7
31	LPMode	O O	ModSell	8
30	Vcc1	97	Reset	g
29	VccTx	<u>a</u>	VccBx	10
28	IntL	Ū.	SCI	11
27	ModPrsL	<u>ା</u> ଜା	SDA	12
26	GND	Je la	GND	13
25	RX4p		RX3n	14
24	RX4n		RX3n	15
23	GND		GND	16
22	RX2p		RX1n	17
21	RX2n		RX1p	18
20	GND		GND	19

Top Side Viewed from Top

Bottom Side Viewed from Bottom

#### **Pin Assignment**

Logic	Symbol	Description	Notes
	GND	Ground	1
CML-I	Tx2n	Transmitter Inverted Data Input	
CML-I	Tx2p	Transmitter Non-Inverted Data output	
	GND	Ground	1
CML-I	Tx4n	Transmitter Inverted Data Input	
CML-I	Tx4p	Transmitter Non-Inverted Data output	
	GND	Ground	1
LVTLL-I	ModSelL	Module Select	
LVTLL-I	ResetL	Module Reset	
	VccRx	+3.3V Power Supply Receiver	2
LVCMOS-I/O	SCL	2-Wire Serial Interface Clock	
LVCMOS-I/O	SDA	2-Wire Serial Interface Data	
	GND	Ground	
CML-O	Rx3p	Receiver Non-Inverted Data Output	
	Logic  CML-I  CML-I  CML-I  CML-I  CML-I  LVTLL-I  LVTLL-I  LVCMOS-I/O  LVCMOS-I/O  CML-O	Logic         Symbol           GND           CML-I         Tx2n           CML-I         Tx2p           CML-I         Tx2p           CML-I         Tx4n           CML-I         Tx4p           CML-I         Tx4p           CML-I         KesetL           LVTLL-I         ModSelL           LVTLL-I         ResetL           LVCMOS-I/O         SCL           LVCMOS-I/O         SDA           CML-O         Rx3p	LogicSymbolGeroundCML-ITx2nTransmitter Inverted Data InputCML-ITx2pTransmitter Non-Inverted Data outputCML-ITx2pGroundCML-ITx4nTransmitter Inverted Data InputCML-ITx4nTransmitter Inverted Data InputCML-ITx4pTransmitter Non-Inverted Data outputCML-IMadbellGroundLVTLL-IModSelLModule SelectLVTLL-IResetLModule ResetLVCMOS-I/OSCL2-Wire Serial Interface ClockLVCMOS-I/OSDA2-Wire Serial Interface DataCML-ORx3pReceiver Non-Inverted Data Output

15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	1
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		VccTx	+3.3 V Power Supply transmitter	2
30		Vcc1	+3.3 V Power Supply	2
31	LVTTL-I	LPMode	Low Power Mode	
32		GND	Ground	1
33	CML-I	Тх3р	Transmitter Non-Inverted Data Input	
34	CML-I	Tx3n	Transmitter Inverted Data Output	
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Output	
38		GND	Ground	1

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#### Notes:

1. GND is the symbol for signal and supply (power) common for QSFP+ modules. All are common within the QSFP+ module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.

2. VccRx, Vcc1 and VccTx are the receiving and transmission power suppliers and shall be applied concurrently.

Recommended host board power supply filtering is shown in Figure 3 below. Vcc Rx, Vcc1 and Vcc Tx may be

internally connected within the QSFP+ transceiver module in any combination. The connector pins are each rated for a

maximum current of 500mA.

#### **Compatibility Test**

In order to ensure the product compatibility, our products will be tested on the switch before shipment. Our modules can compatible with many mainstream brand switches, such as Cisco, Juniper, Extreme, Brocade, IBM, H3C, HP, Huawei, D-Link, Mikrotik, ZTE, TP-Link...

Our test equipment: VOLKTEK MEN-4110, HP 2530-8G, CRS226-24G-25+RM, Catalyst 2960G Series, Catalyst 3850 XS 10G SFP+, Catalyst 3750-E Series, HUAWEI S5700Series, H3C S3100V2 Series, Juniper-EX4200, etc.



#### **Product Production Process**

# **Quality Assurance**

Continuous introduction of new equipment, produced by strict standards, strict quality inspection, to guarantee the high quality standard of each product.



### Packaging

ETU-Link provides two kinds of packaging, 10pcs/Tray and individual package.



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